



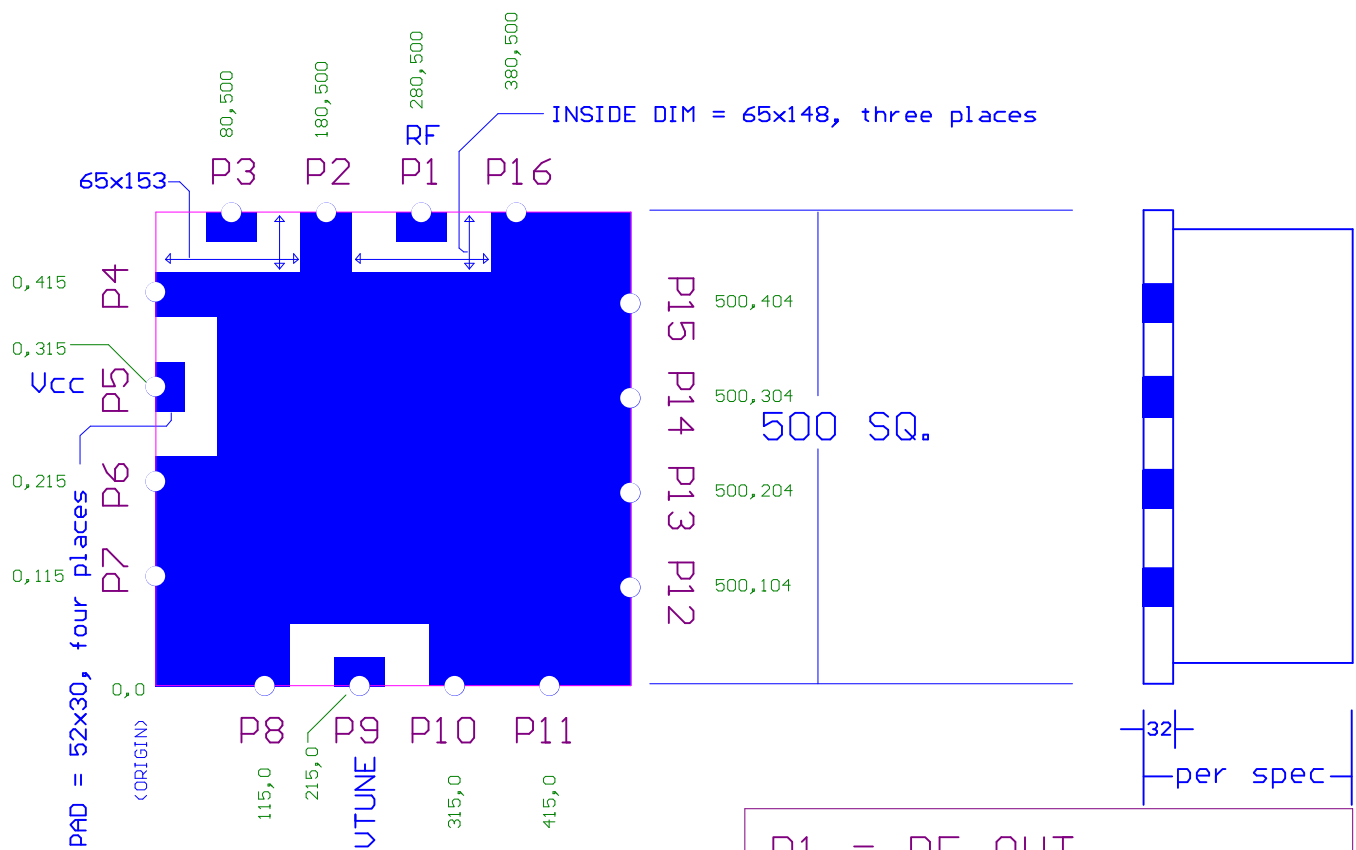
	<u>MIN</u>	<u>TYP</u>	<u>MAX</u>	<u>UNITS</u>	<u>NOTES</u>	<u>Production Test</u>	<u>First Article Test</u>
Frequency Range	6140		7120	MHz	1	..	..
Tuning Voltage	1		18	V	1	..	..
Modulation Sensitivity		80		MHz/V	1	..	..
Operates with Vtune = 0V					1	..	..
Output Power - Fundamental	-7	-2	3	dBm	1	..	..
Output Power - Half Frequency	-10	-5	0	dBm	3	..	..
Phase Noise @							
1 KHz Offset		-65	-60	dBc/Hz	1	..	..
10 KHz Offset		-93	-88	dBc/Hz	1	..	..
100 KHz Offset		-113	-106	dBc/Hz	1	..	..
1 MHz Offset		-133	-128	dBc/Hz	1	..	..
10 MHz Offset				dBc/Hz	1	..	..
Pushing		2		MHz/V	3	..	..
Pulling (12dB RL, any phase)		1		MHz _{p-p}	3	..	..
Modulation Bandwidth		5		MHz	4	..	..
Tuning Port Capacitance		47		pF	2	..	..
Harmonics		-15	-8	dBc	3	..	..
Sub-Harmonics		-20	-10	dBc	3,7	..	..
Vcc		4.5		V			
Icc		50		mA	1	..	..
Operating Temperature	-40		+85	C	3	..	..
Package		SMUA_DBL			6	..	..
LxW		0.500		inch sq.	5	..	..
Height		0.085		inch	5	..	..

Notes

1. Tested in production, and guaranteed
2. Per current design, not a tested parameter
3. Sample tested from production lot
4. Not a tested or guaranteed design goal
5. By inspection
6. RoHS compliant
7. Design goal subject to change

DBL FOOTPRINT

TOP VIEW, BOT SIDE



NOTES

1. Bottom is solid ground plane.
2. All dimensions in mils
3. All holes are plated through.

P1 = RF OUT
P3 = 1/2 FREQUENCY
P5 = Vcc
P9 = VTUNE
ALL OTHER PINS GND