



VCO R6750SMUA5CR_DBL

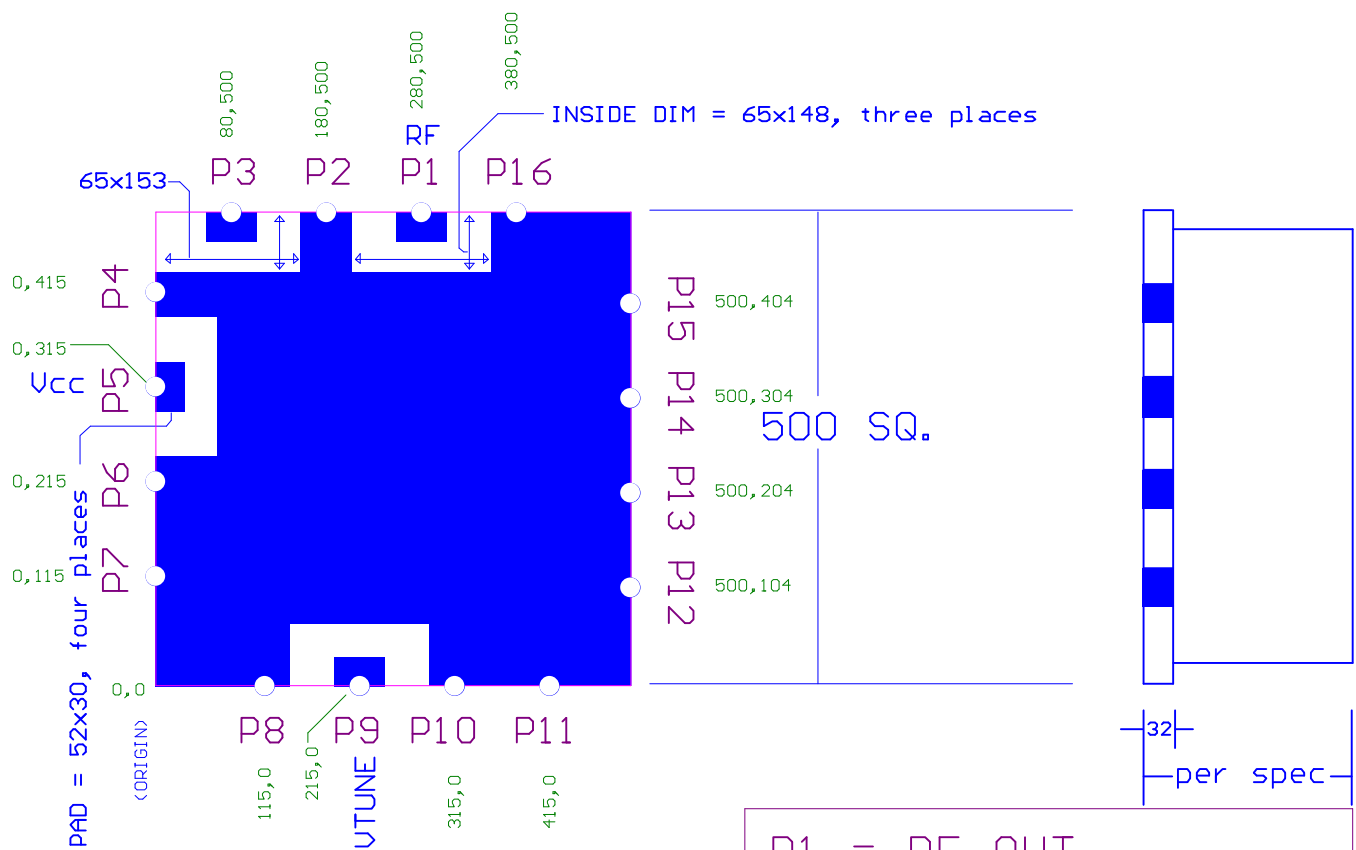
rev_0initialrev

						Production	First Article
	<u>MIN</u>	<u>TYP</u>	<u>MAX</u>	<u>UNITS</u>	<u>NOTES</u>	<u>Test</u>	<u>Test</u>
Frequency Range		6750		MHz	1	..	..
Tuning Voltage	0.5		4.5	V	1	..	..
ModulationSensitivity		11		MHz/V	1,7	..	..
Operates with Vtune = 0V					1	..	..
Output Power - Fundamental	-4.5	-2	0.5	dBm	1	..	..
Output Power - Half Frequency	-10	-5	0	dBm	3	..	..
Phase Noise @							
1 KHz Offset		-80	-75	dBc/Hz	1	..	..
10 KHz Offset		-107	-102	dBc/Hz	1	..	..
100 KHz Offset		-127	-122	dBc/Hz	1	..	..
1 MHz Offset		-147	-142	dBc/Hz	1	..	..
10 MHz Offset		-164	-155	dBc/Hz	1	..	..
Pushing		1		MHz/V	3	..	..
Pulling (12dB RL, any phase)		1		MHz _{p-p}	3	..	..
Modulation Bandwidth		1		MHz	4	..	..
Tuning Port Capacitance		18		pF	2	..	..
Harmonics		-15	-10	dBc	3	..	..
Sub-Harmonics		-20	-10	dBc	3	..	..
Vcc		5.0		V			
Icc		55		mA	1	..	..
Operating Temperature	-40		+85	C	3	..	..
Package		SMUA_DBL			6	..	..
LxW		0.500		inch sq.	5	..	..
Height		0.220		inch	5	..	..

- Notes**
1. Tested in production, and guaranteed
 2. Per current design, not a tested parameter
 3. Sample tested from production lot
 4. Not a tested or guaranteed design goal
 5. By inspection
 6. RoHS compliant
 7. Design goal subject to change

DBL FOOTPRINT

TOP VIEW, BOT SIDE



NOTES

1. Bottom is solid ground plane.
2. All dimensions in mils
3. All holes are plated through.

P1 = RF OUT
 P3 = 1/2 FREQUENCY
 P5 = VCC
 P9 = VTUNE
 ALL OTHER PINS GND