

DSPEC NO.		ISSUED DATE	2023.03.27	PUBLISHED BY
PRODUCT NAME	J 3570A	VERSION	d01	
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# SPECIFICATION

SPEC NO. : \_\_\_\_\_

PART NO. : \_\_\_\_\_

PRODUCT NAME : J 3570A

DESCRIPTION : Halogen Free RoHS Product

REVISION STATUS

VERSION	DATE	PAGE	REVISION DESCRIPTION	PREPARED	CHECKED	APPROVED
d01	2023.03.27	All	New	曾靳毅	沈廷宜	陳勁豪

Prepared By	Checked By	Approved By

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## ***CIROCOMM TECHNOLOGY CORP.***

### **PART NUMBER:**

ELECTRICAL CHARACTERISTICS:

This filter satisfies Table 1 at Temperature Range : -40 to +100°C

CENTER FREQUENCY :  $f_c = 3570$  MHz

PASSBAND WIDTH : 3470~3670 MHz

INPUT/OUTPUT IMPEDANCE :  $50\ \Omega$

Max. INPUT POWER : 1 W

Moisture Sensitivity Level (MSL): 2a

TABLE 1

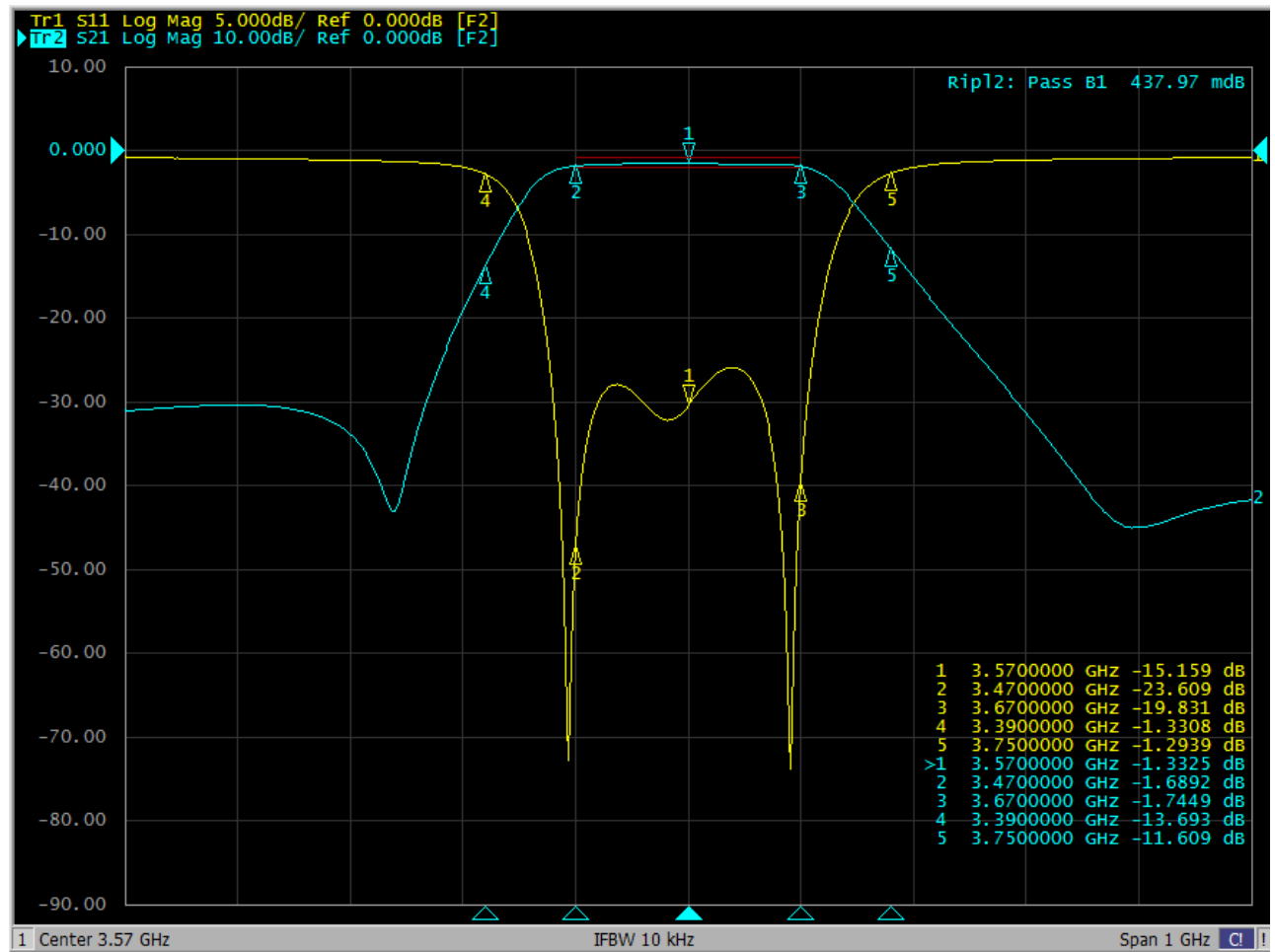
NO.	ITEM		SPECIFICATION		
			Min	Typ.	Max
1	PASS BAND INSERTION LOSS			2.0 dB	2.5 dB
2	PASS BAND RIPPLE			1.2 dB	1.5 dB
3	PASS BAND RETURN LOSS		10 dB	12 dB	
4	STOP—BAND ATTENUATION	at 3390 MHz	8 dB	10 dB	
		at 3750 MHz	8 dB	10 dB	
Item NO.4 specifies the absolute value of attenuation.					

**※Data is measured on Cirocomm EVB board**



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## TYPICAL ELECTRICAL CHARACTERISTICS



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## ***CIROCOMM TECHNOLOGY CORP.***

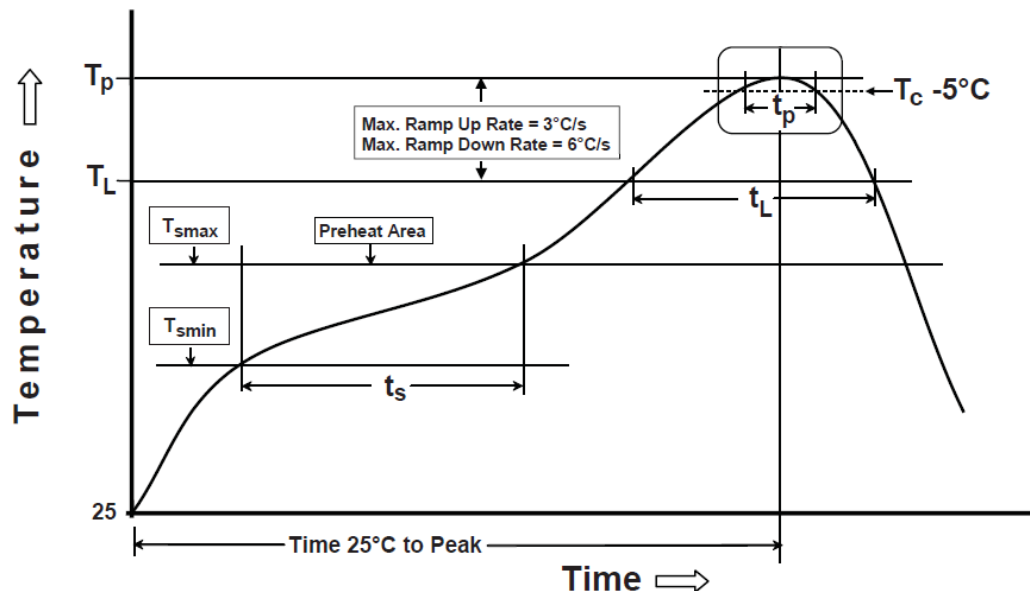
### **2. Recommended Reflow Soldering Profile**

Cirocomm products can be assembled following Pb-free assembly. According to the Standard IPC/JEDEC J-STD-020C, the temperature profile suggested is as follow:

Phase	Profile features	Pb-Free Assembly (SnAgCu)
PREHEAT	-Temperature Min(Tsmin) -Temperature Max(Tsmax) -Time(ts) form (Tsmin to Tsmax)	150℃ 200℃ 60-120 seconds
RAMP-UP	Avg. Ramp-up Rate (Tsmax to TP)	3℃/second(max)
REFLOW	-Temperature(TL) -Total Time above TL (t L)	217℃ 30-100 seconds
PEAK	-Temperature(TP) -Time(tp)	260℃ 3 second
RAMP-DOWN	Rate	6℃ / second max.
Time from 25℃ to Peak Temperature		8 minutes max.
Composition of solder paste		96.5Sn/3Ag/0.5Cu
Solder Paste Model		SHENMAO PF606-P26

Note : All the temperature measure point is on top surface of the component, if temperature over recommend, it will make component surface peeling or damage.

The graphic shows temperature profile for component assembly process in reflow ovens



### **Soldering With Iron:**

Soldering condition : Soldering iron temperature  $270 \pm 10$  °C.

Apply preheating at 120℃ for 2-3 minutes. Finish soldering for each terminal within 3 seconds, if soldering iron over temperature  $270 \pm 10$  °C or 3 seconds, it will make component surface peeling or damage.

Soldering iron cannot leakage of electricity.

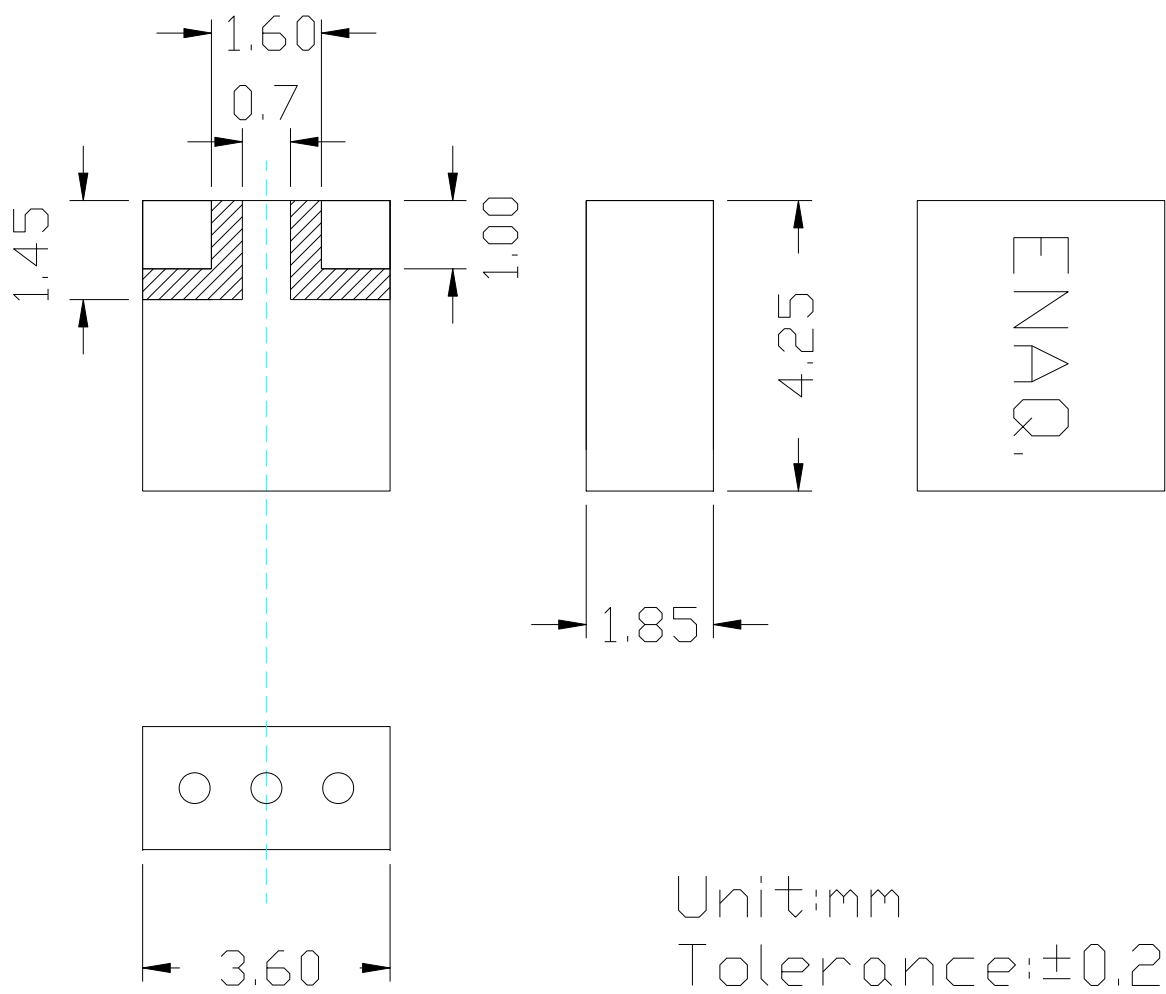


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***CIROCOMM TECHNOLOGY CORP.***

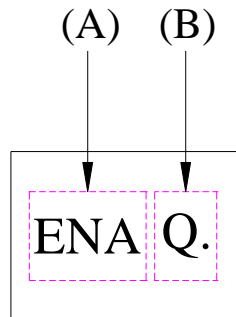
**3. DIMENSION AND PCB LAYOUT**

**3-1 SHAPE AND DIMENSION**



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### 3-1-2 MARKING



(A) Product name : ENA for 3570A

(B) Year/Month : Please refer to the Table-2

**(Table-2)**

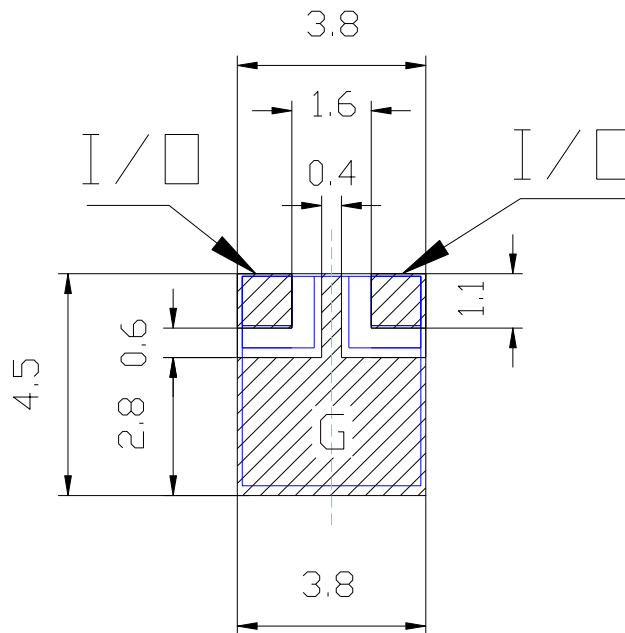
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	2	B		2	P		2	B .		2	P .
	3	C		3	Q		3	C .		3	Q .
	4	D		4	R		4	D .		4	R .
	5	E		5	S		5	E .		5	S .
	6	F		6	T		6	F .		6	T .
	7	G		7	U		7	G .		7	U .
	8	H		8	V		8	H .		8	V .
	9	J		9	W		9	J .		9	W .
	10	K		10	X		10	K .		10	X .
	11	L		11	Y		11	L .		11	Y .
	12	M		12	Z		12	M .		12	Z .

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### 3-2 PCB RECOMMENDED PATTERN FOR FILTER

**Note: Test PCB material: ROGERS (RO4003C), 0.5mm.**

**The filter use limit: the layout goes away PCB edge.**



Filter Outline



I/□ Pads must be connected to lines with  $50\Omega$  impedance. In the applicationa termination of  $50\Omega$  must be realized.