

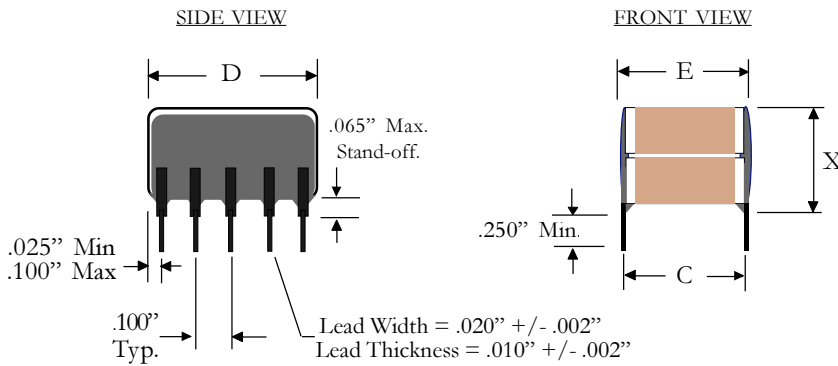
**1. SCOPE**

This specification covers the characteristics of the “ST” series size 5550, 200°C (Class II) dielectric, 3.0 uF capacitance, +/- 10% tolerance, 600 VDC rated stacked ceramic chip capacitors with straight through-hole tinned-copper leads.

**2. DIELECTRIC CHARACTERISTICS**

Operating Temperature Range: -55°C to +200°C  
 Temperature Coefficient: +15 -65% Change in capacitance @ 1 Vrms  
 Dissipation Factor: 2.5% Max. @ 25°C  
 Insulation Resistance, 25°C: 100 Giga-Ohms or 1000 Ohm-Farads, whichever is less  
 Insulation Resistance, 200°C: 1 Giga-Ohms or 10 Ohm-Farads, whichever is less  
 Dielectric Withstanding Voltage: 750VDC  
 Aging Rate: 2.0% per decade  
 Test Parameters: 1KHz, 1.0 Volts +/- 0.2 Vrms, 25°C  
 Lead Material: Tinned-Copper

**3. PHYSICAL DIMENSIONS (NOT SCALED)**



Each chip is 1.5 uF and thickness of each chip is 0.140" Min. & 0.175" Max.

dimensions	E	D	X	C	Number of Leads per Side	Number of chips Stacked
inches (mm)	.630 (16.0)	.500 (12.7)	.430 (10.9)	.580 (14.7)	5	2
+/- in (mm)	Max	.025 (.635)	Max	.025 (.635)	n/a	n/a

**4. PART NUMBER DESCRIPTION**

**ST 5550 E 305 K 601 LN X - A 2**

ST = Stacked Capacitors Assembly  
 5550 = Size  
 E = 200°C (Class II) Dielectric  
 305 = Capacitance Code (pF): First two digits are significant, third digit denotes number of zeros  
 K = Capacitance tolerance: K = +/- 10%  
 601 = DC Voltage Rating: First two digits are significant, third digit denotes number of zeros  
 LN = Through-hole Leads, Straight  
 X = Overall Height: .430" Max. (10.9mm Max.)  
 - A2 = Special Stacked Assembly: (Letter “A” designates tinned-copper leads & the number designates quantity of capacitors in the assembly)

REVISION: A

NOVACAP P/N: ST5550E305K601LNX-A2  
 CUSTOMER:  
 CUST. P/N:

SPEC DESIGNER MK 02/21/22  
 SPEC MANAGER VL 02/21/22  
 ENGINEERING VL 02/21/22